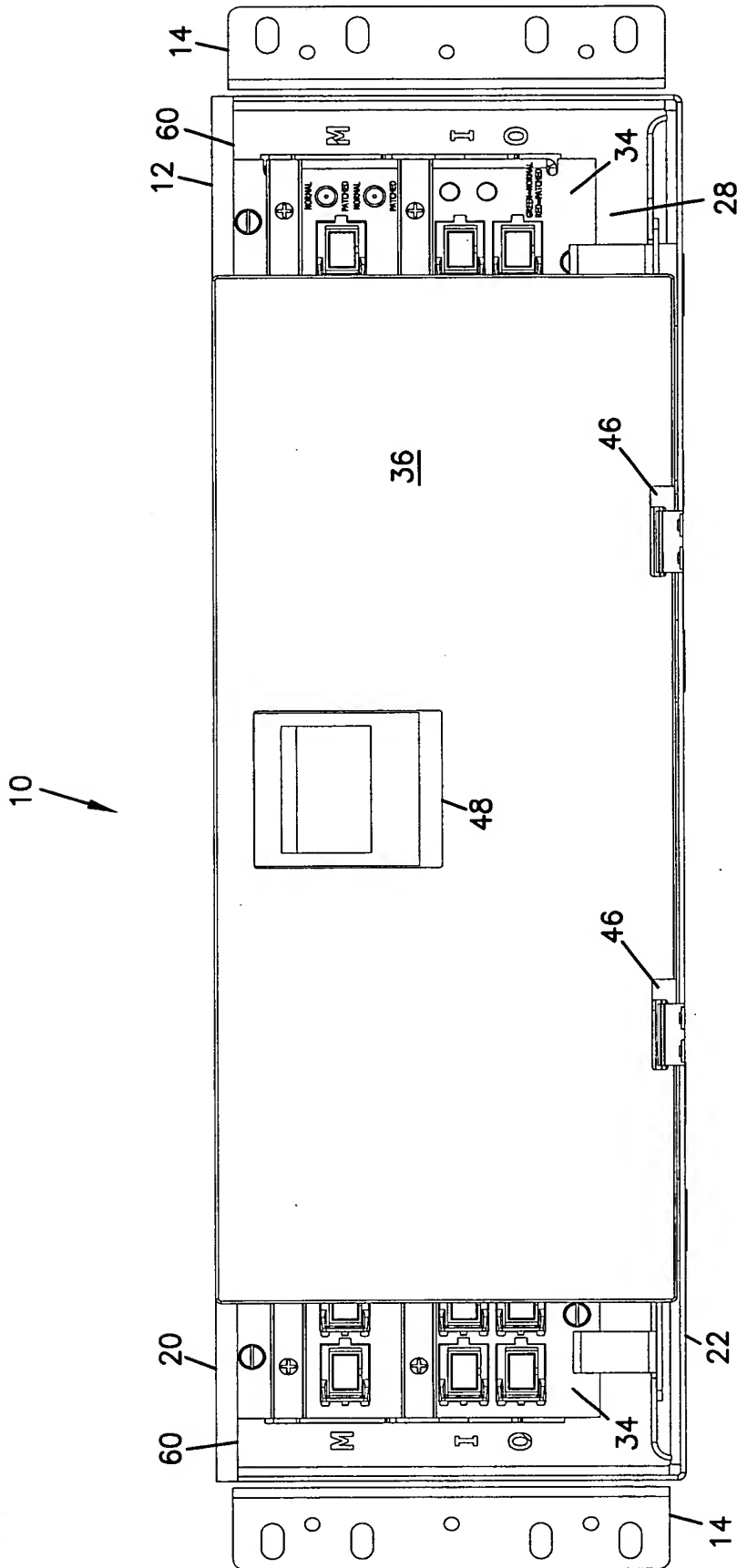




FIG.1



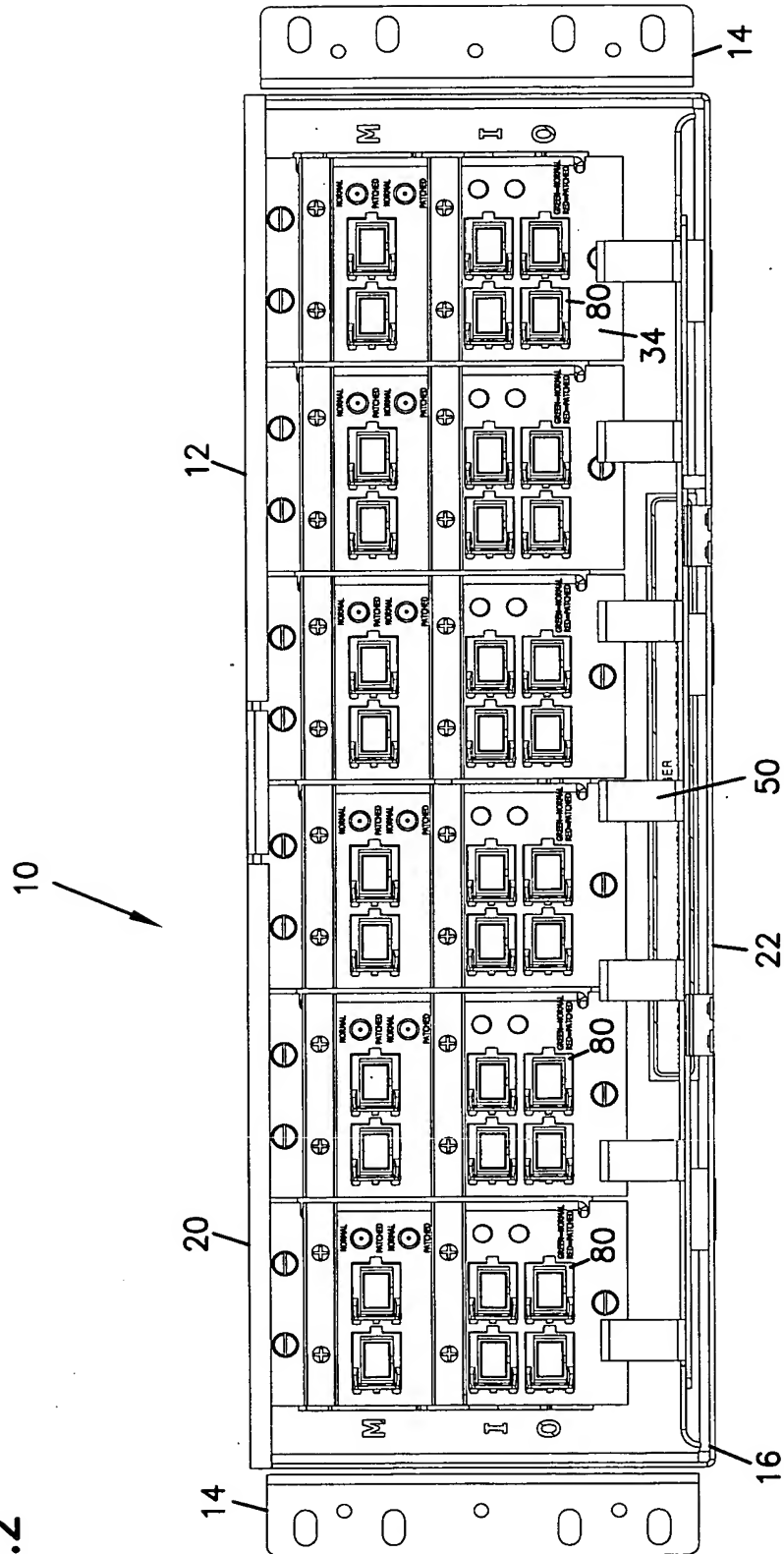
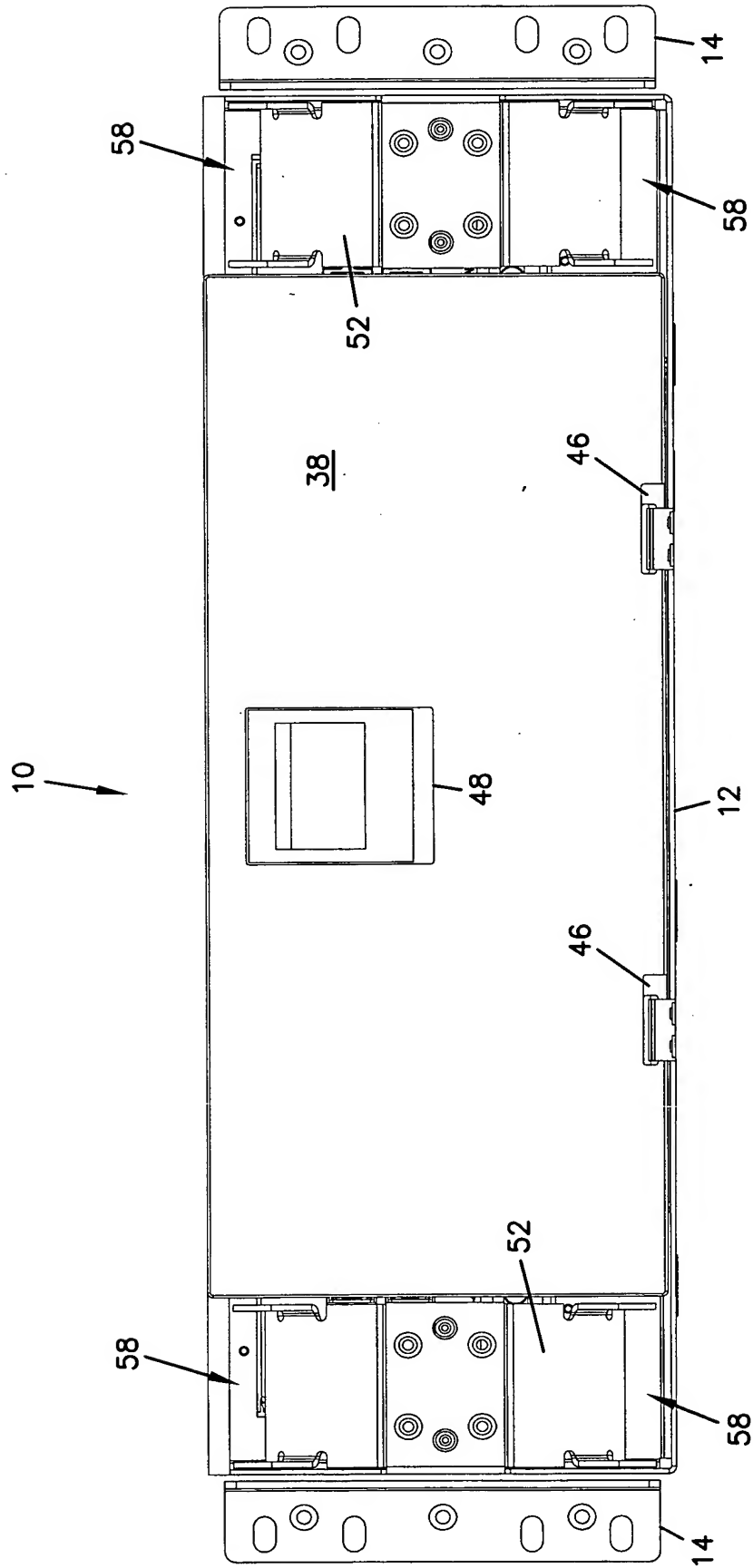


FIG.3



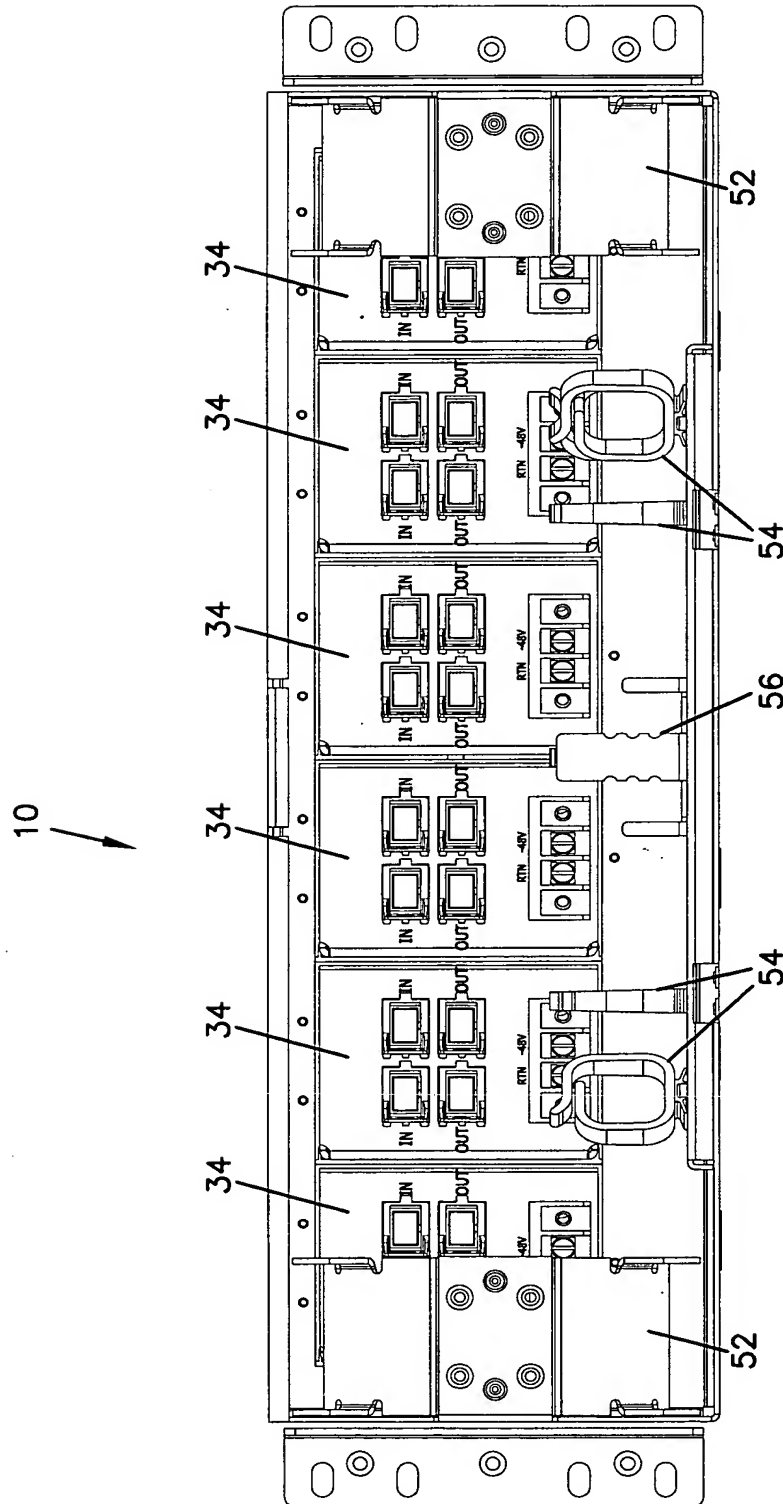
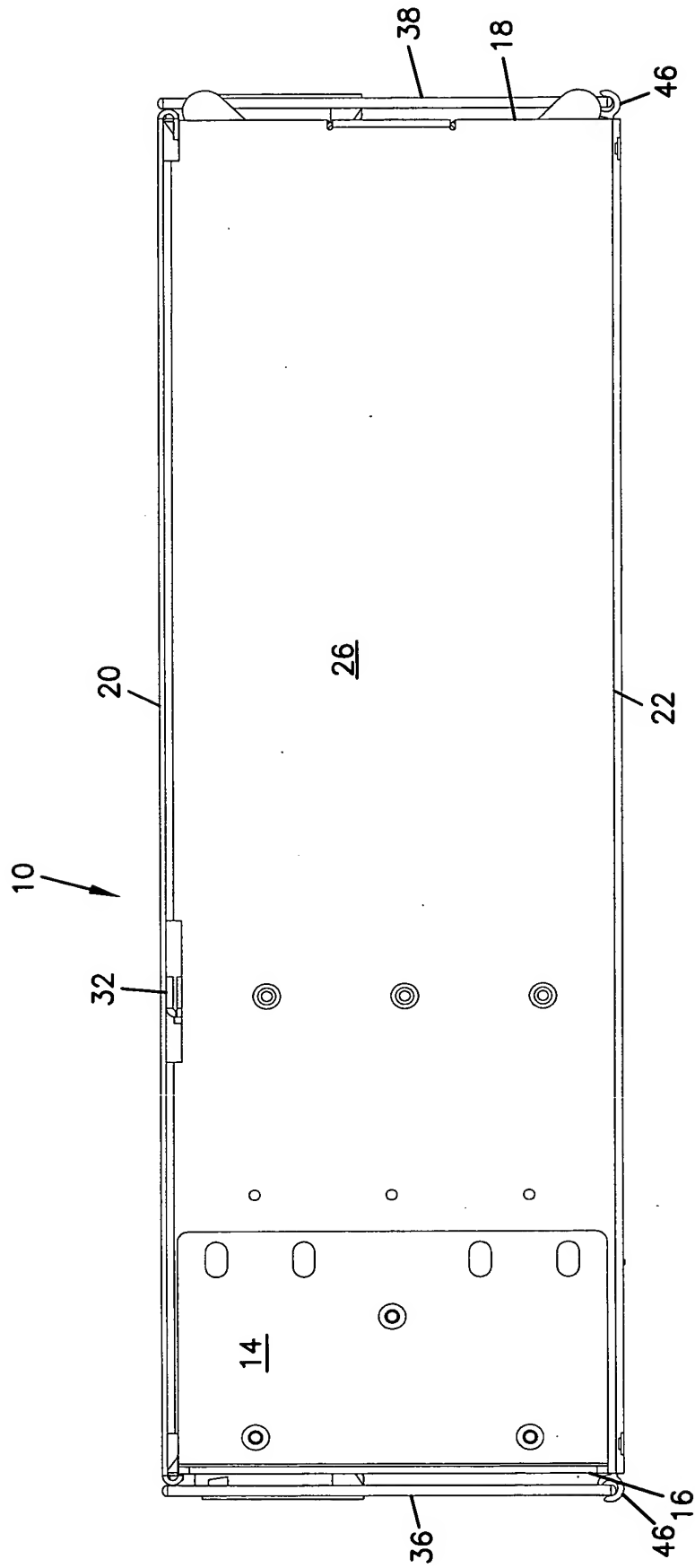


FIG.5



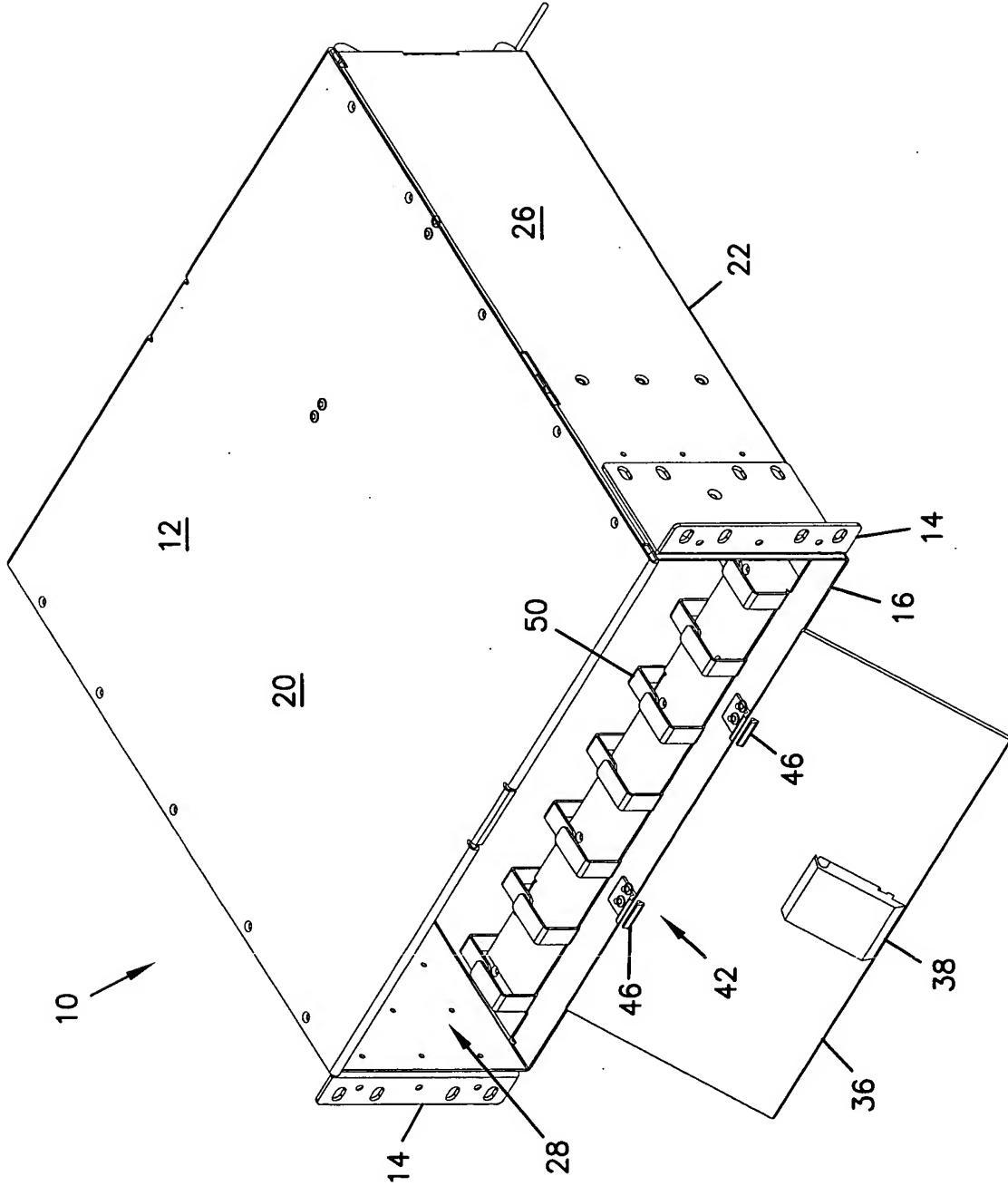


FIG. 6

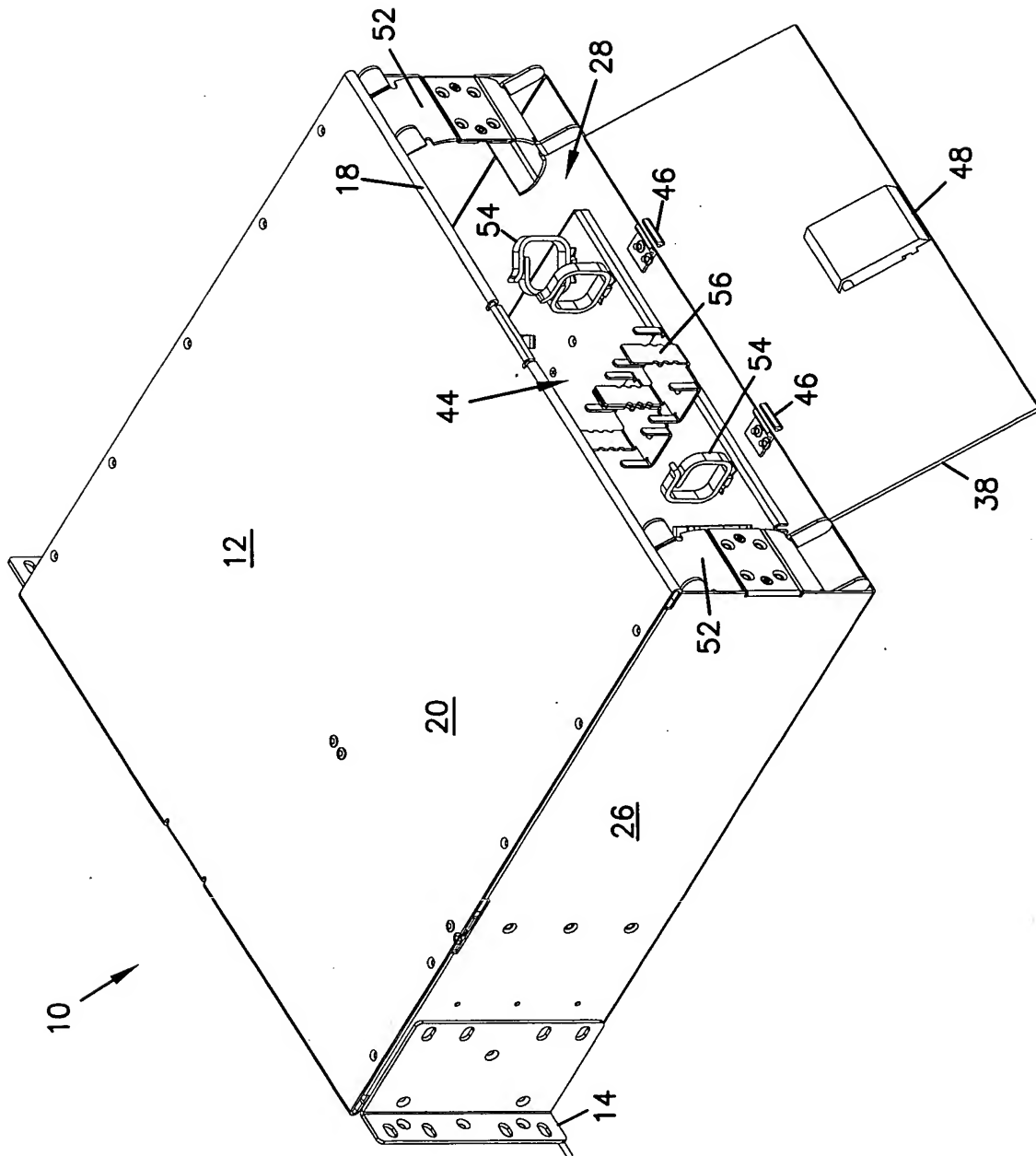


FIG.8

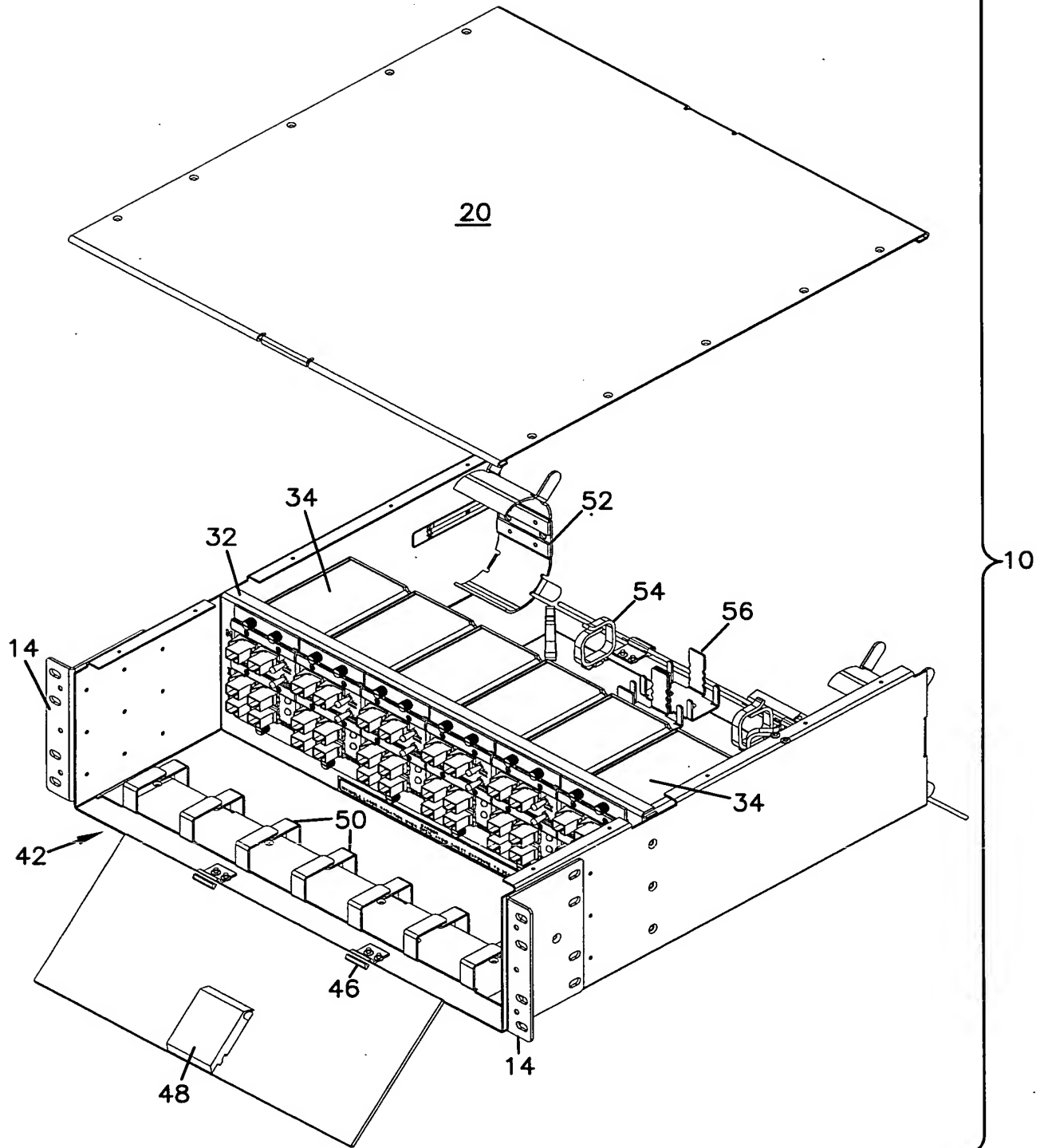




FIG.9

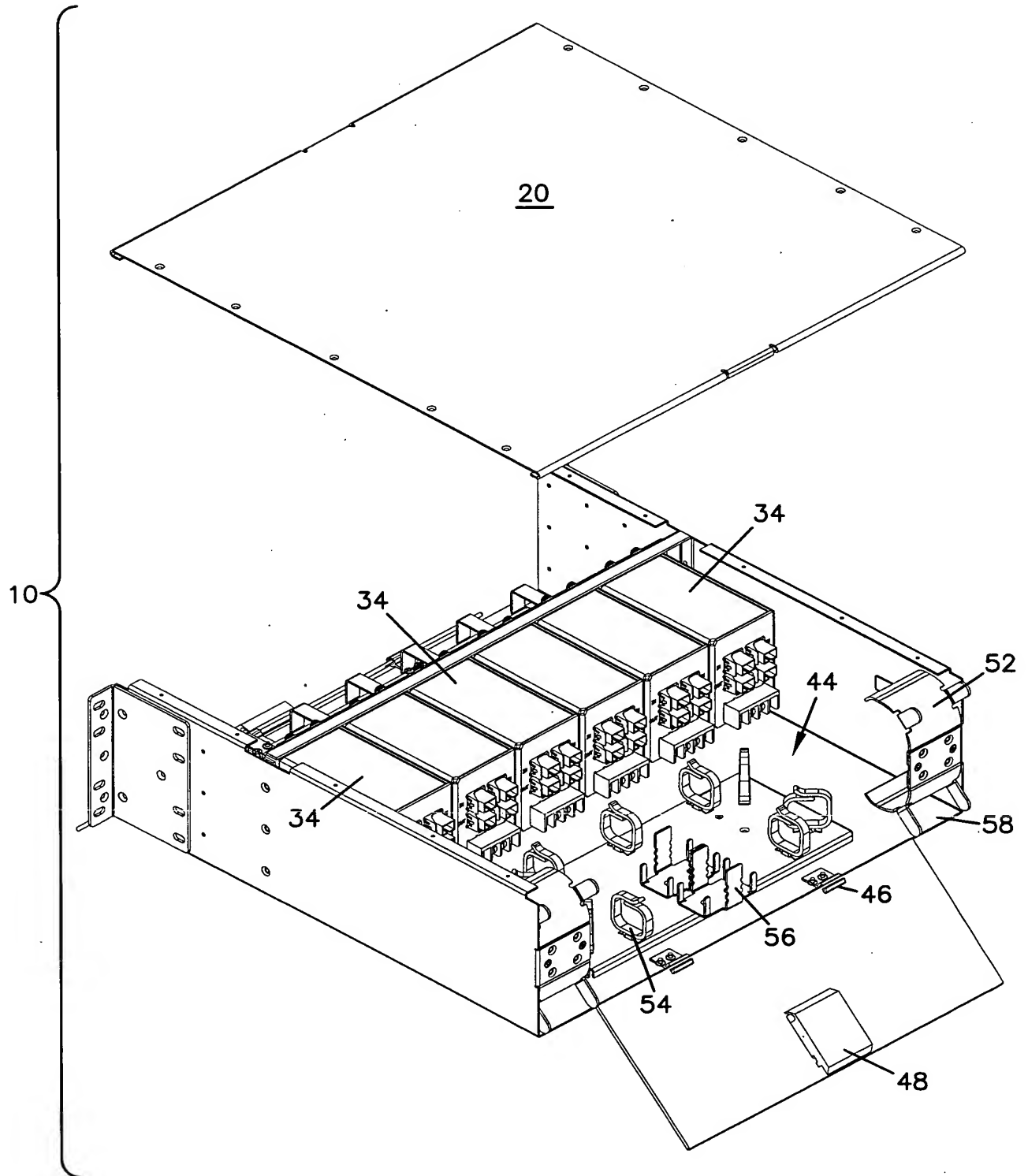
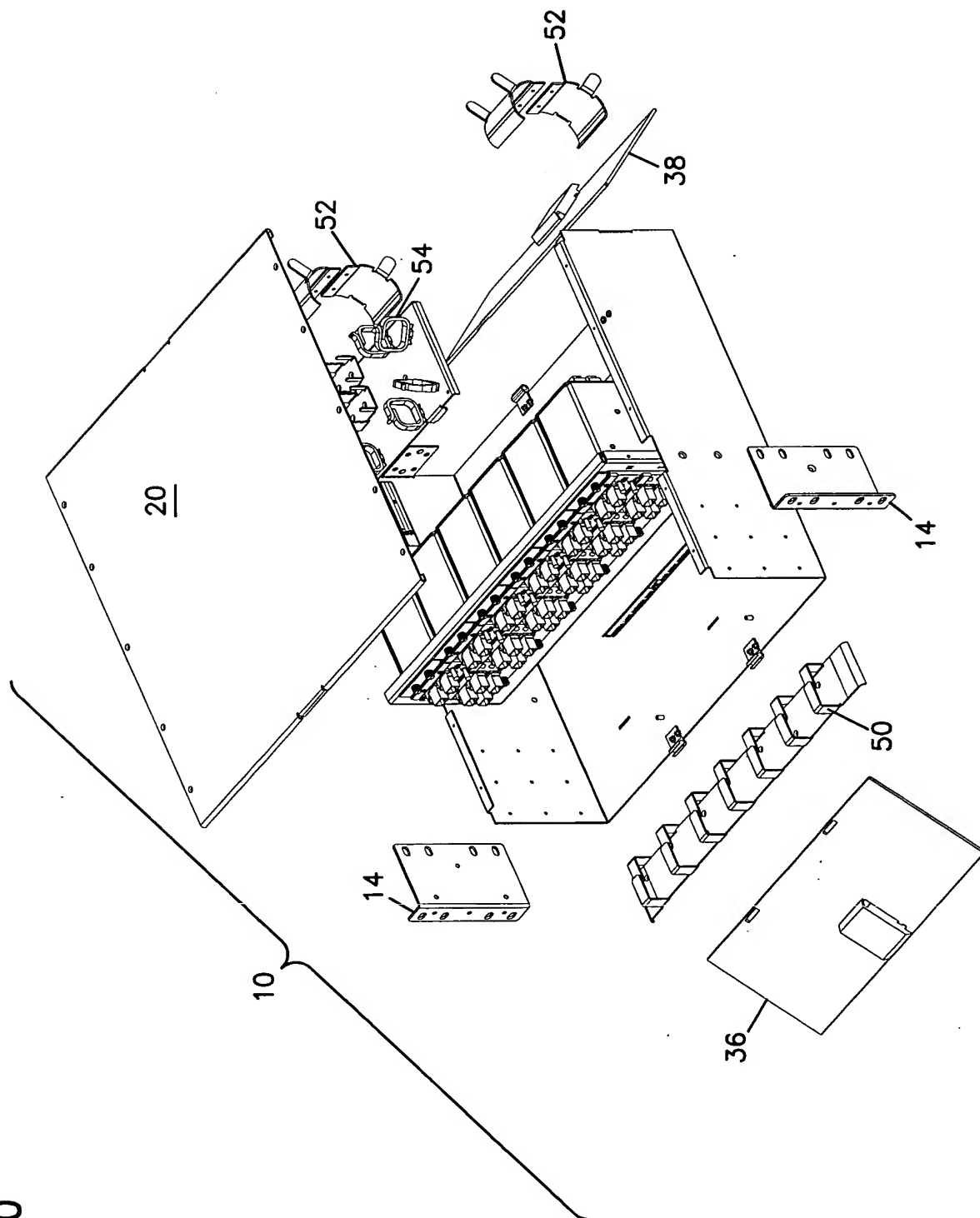


FIG.10



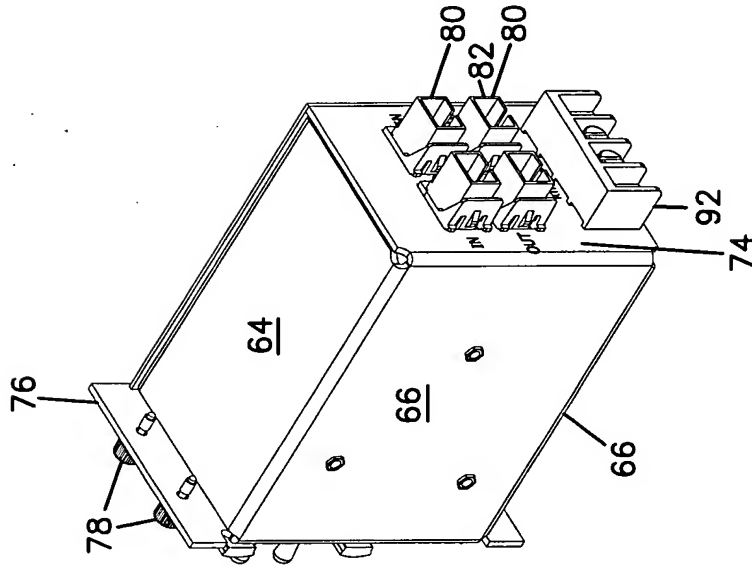


FIG. 12

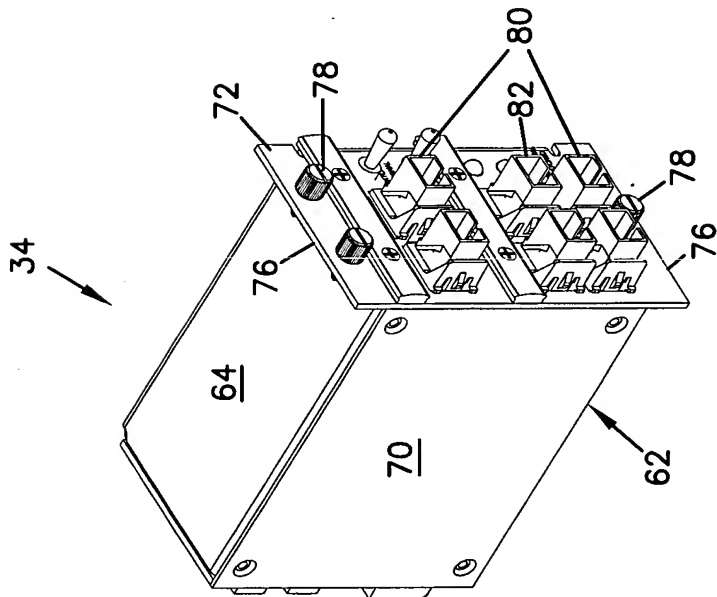


FIG. 11

FIG.14

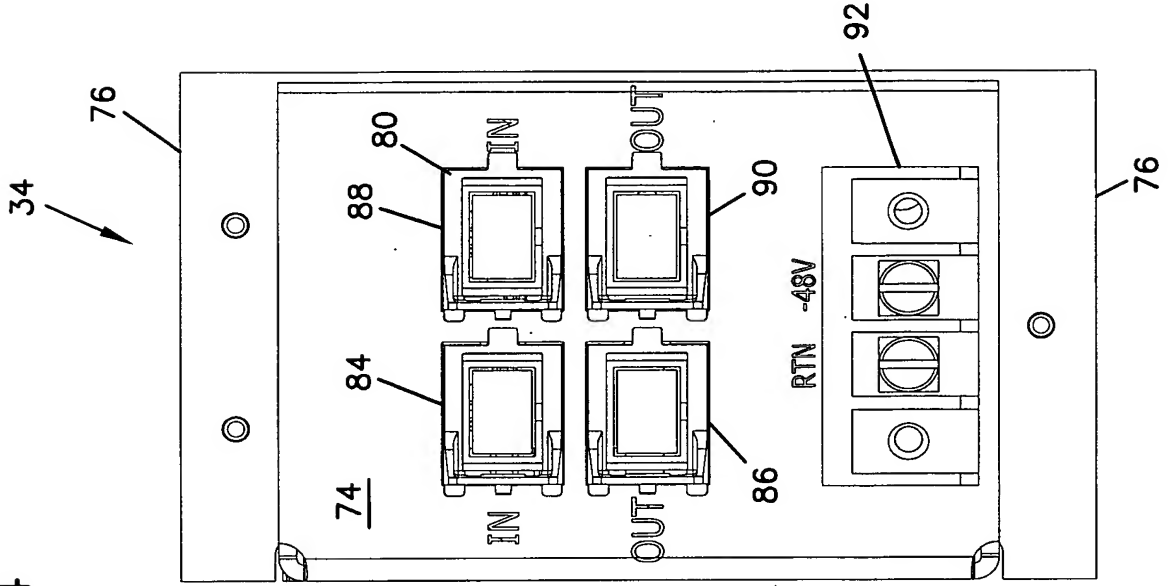
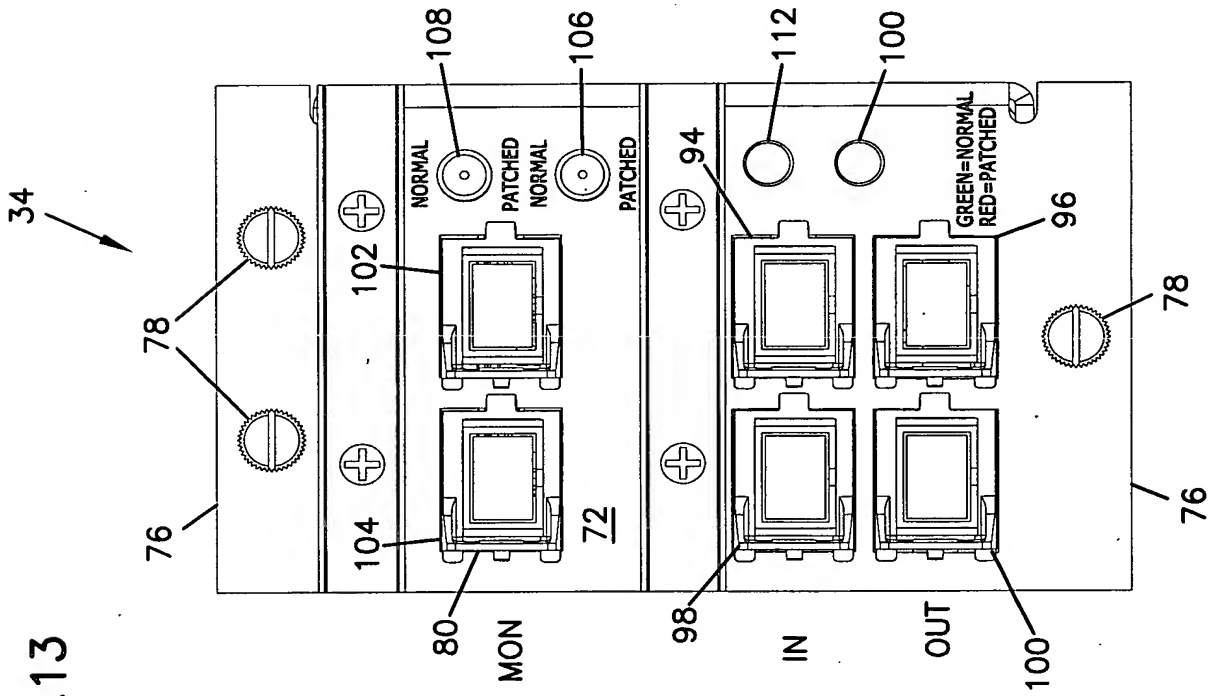
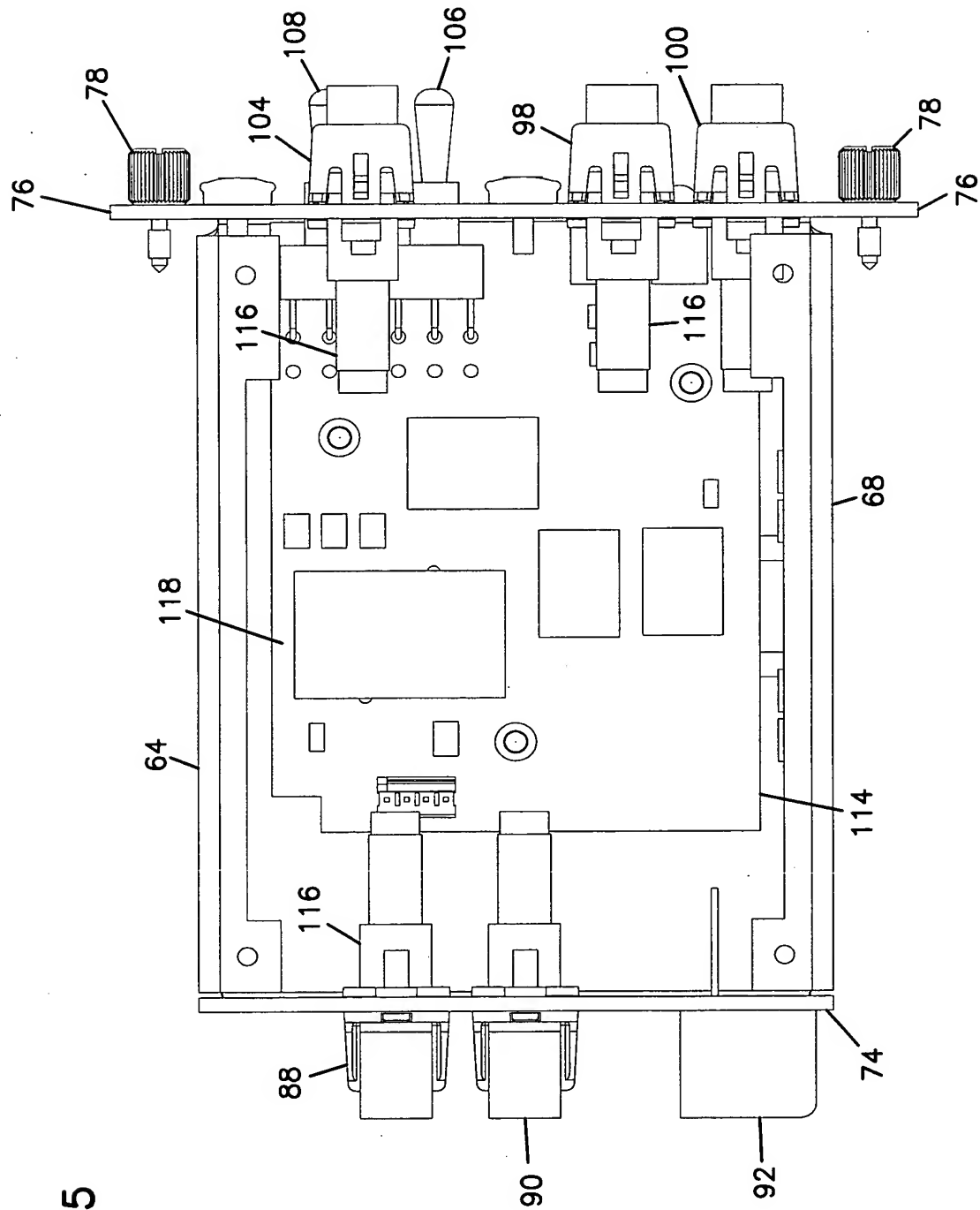


FIG.13





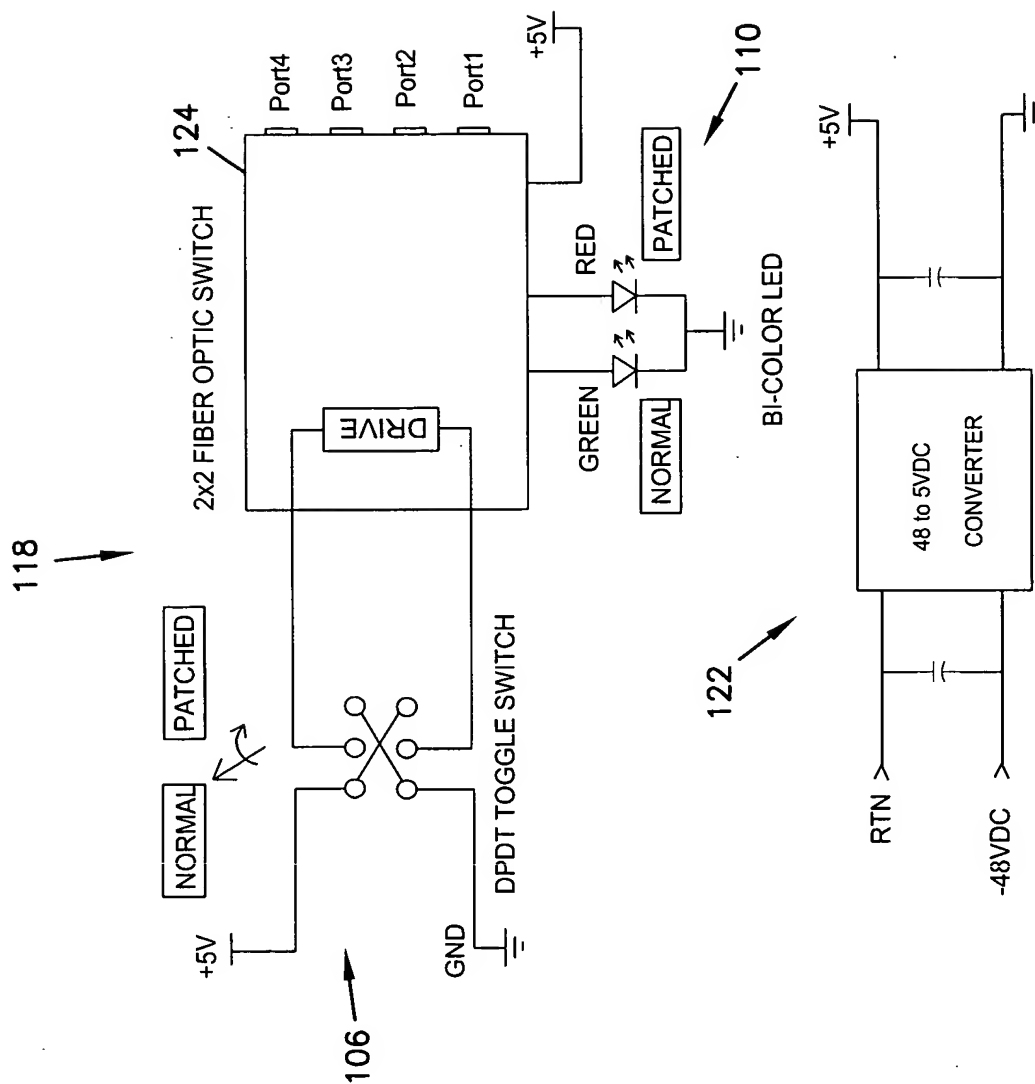


FIG.17

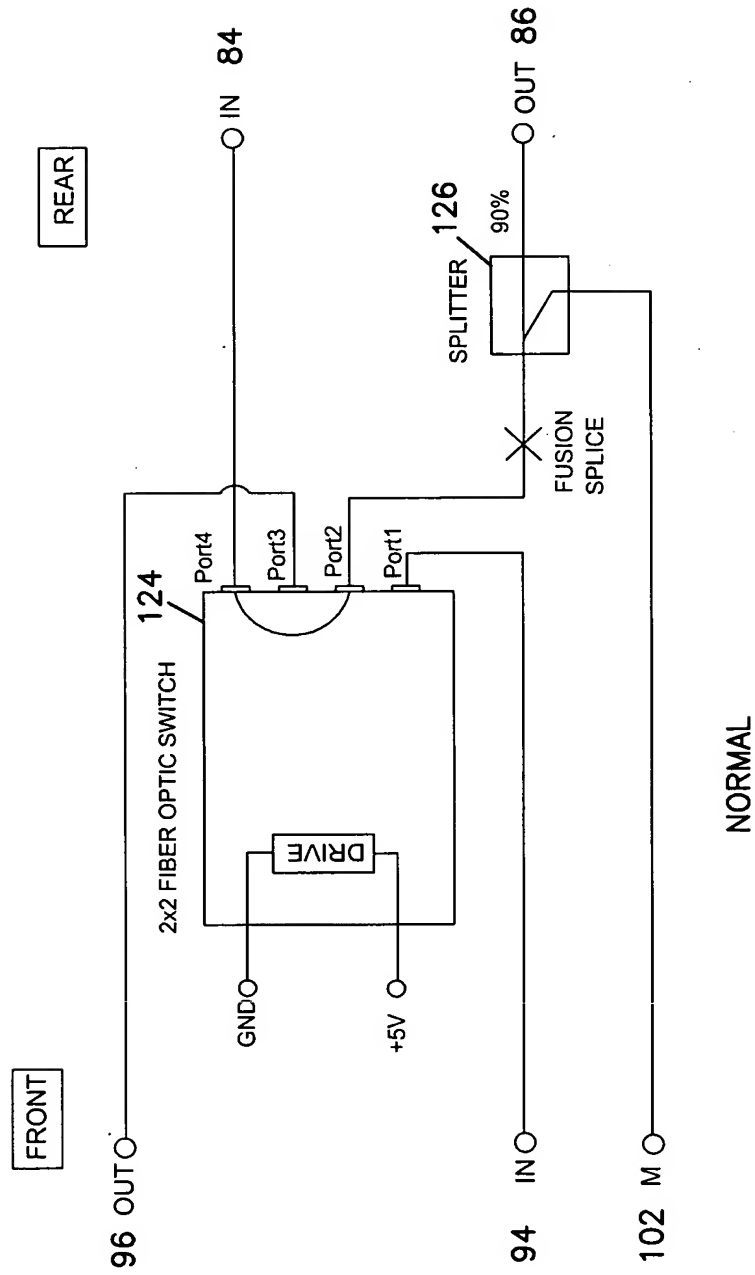


FIG.18

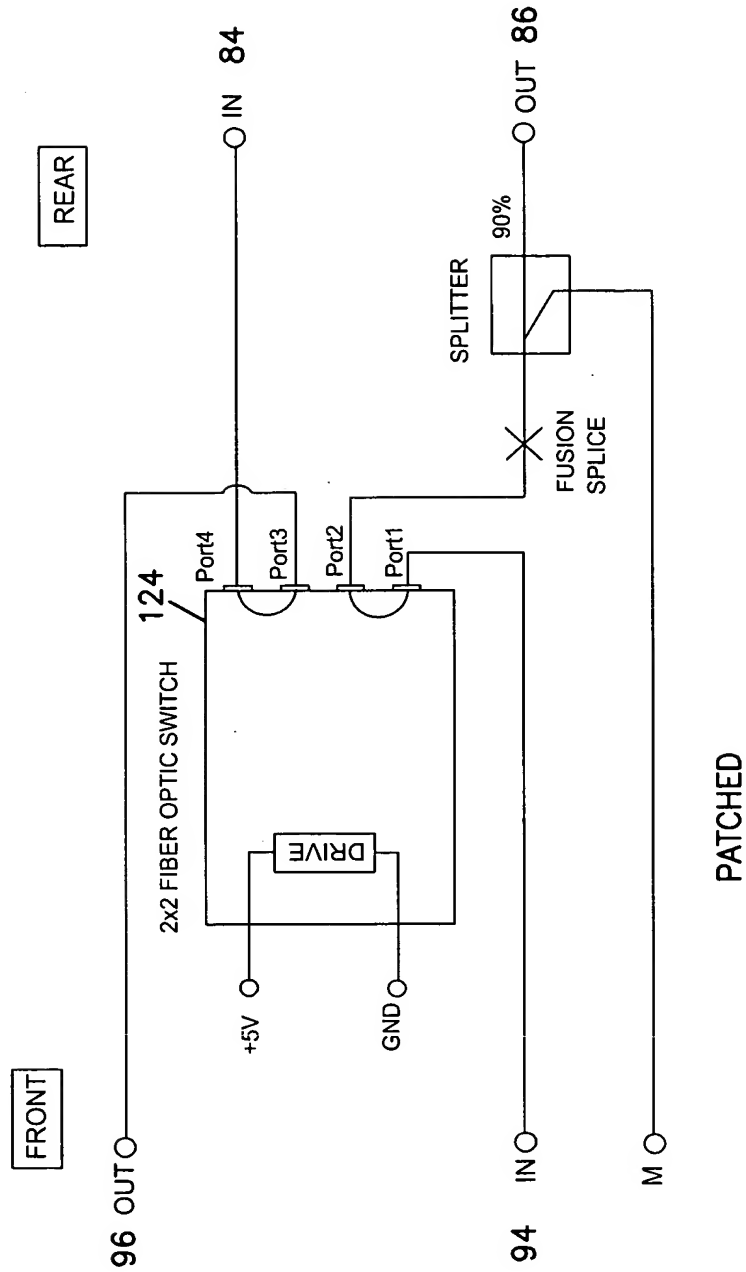


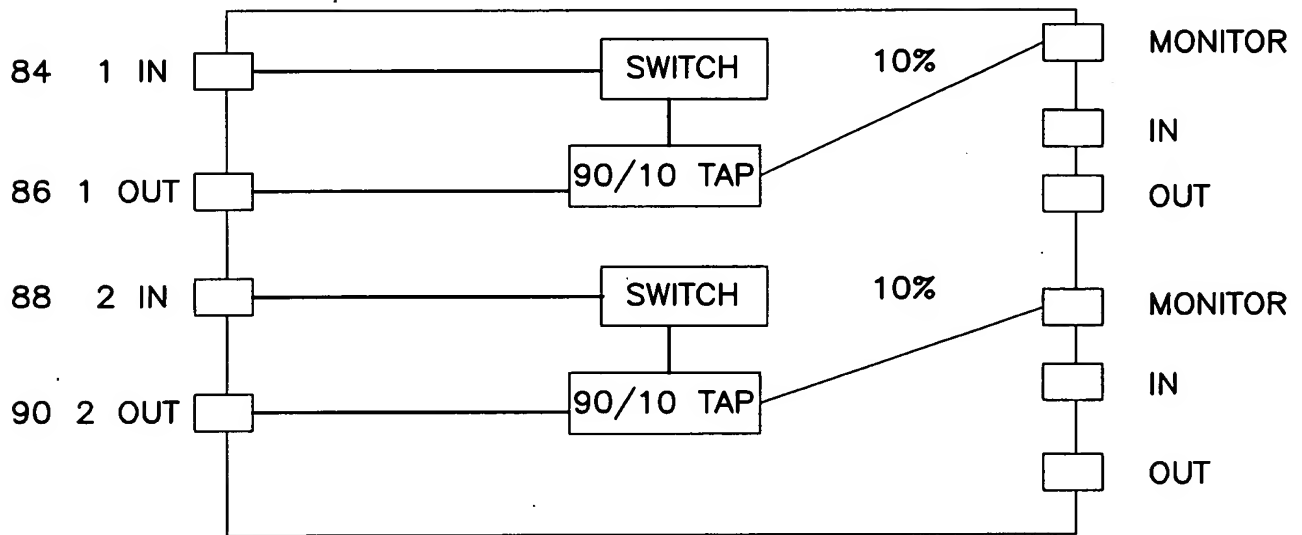


FIG.19

BACK OF MODULE

34

FRONT OF MODULE



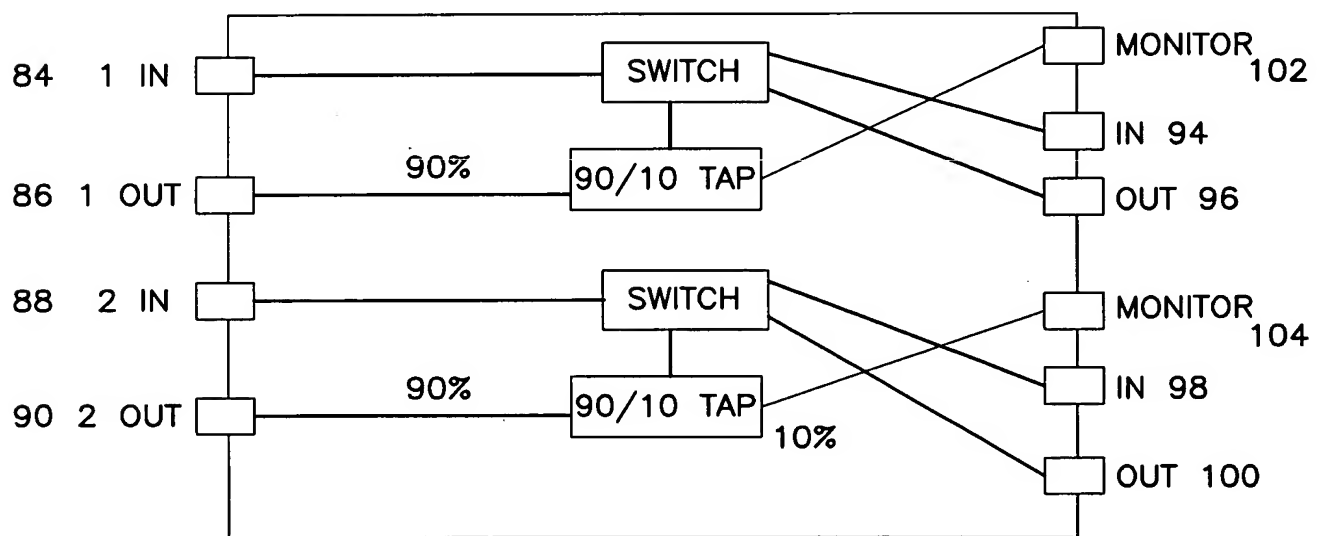
NORMAL

FIG.20

BACK OF MODULE

34

FRONT OF MODULE



PATCHED

FIG.21

